<u>REMARKS</u>

In an Office Action mailed on March 16, 2005, claims 1-5, 9-14, 18-23 and 27 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Choi in view of Firooz; claims 6, 15 and 24 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Choi and Firooz and further in view of Krau; and claims 7, 8, 16, 17, 25 and 26 were indicated as being dependent upon rejected base claims but were indicated as being allowable if rewritten in independent form. Claims 28-31 were indicated as being allowable if rewritten in independent form. However, Applicant points out that claim 28 is an independent claim; and for at least this reason, allowance of claims 28-31 is requested. The § 103 rejections are discussed below.

<u>Rejections of Claims 1-5 and 9:</u>

As amended, the method of independent claim 1 recites comparing computer system configuration data sections of the first and second basic input/output system images and performing the modification of the first basic input/output system image based on this comparison.

Contrary to the limitations of independent claim 1, the cited art fails to teach or suggest the comparing of claim 1. More specifically, Choi generally teaches determining a checksum of a new ROM BIOS image data to determine whether the checksum is correct. *See, for example,* Choi, 3:4-20. However, Choi fails to, for example, fails to teach comparing compatibility of the old and new disclosed user information areas. Applicant also points out that Krau also discloses checking compatibility of BIOS versions but does not teach or suggest determining any compatibility between configuration data sections of BIOS images.

Thus, for at least the reason that are set forth above, none of the cited art references, alone or in combination, teach or suggest the comparing and modifying acts of claim 1. As such, allowance of claims 1-5 and 9 is requested.

<u>Rejections of Claims 10-14 and 18:</u>

As amended, the computer system of independent claim 10 includes a processor that compares a configuration data section of a replacement basic input/output system image with a configuration data section of an existing basic input/output system image.

See discussion of independent claim 1 above. In particular, none of the cited art, alone or in combination, teaches or suggests instructions to perform the comparison as recited in amended independent claim 10. As such, Applicant submits that claim 10 is in condition for allowance. Therefore, allowance of claims 10-14 and 18 is requested.

Rejections of Claims 19-23 and 27:

As amended, the article of independent claim 19 includes a computer readable storage medium that stores instructions to cause a processor to compare a configuration data section of an existing basic input/output system image with a configuration data section of a replacement basic input/output system image.
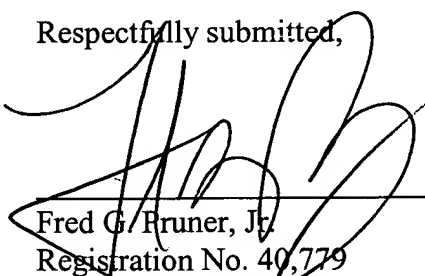
See discussion of independent claim 1 above. In particular, none of the cited references, alone or in combination, teach or suggest instructions to cause a processor to compare configuration data sections from existing and replacement basic input/output system images and modify the replacement basic input/output system image based on this comparison. As such, allowance of claims 19-23 and 27 is requested.

CONCLUSION

In view of the foregoing, withdrawal of the § 103 rejections and a favorable action in the form of a Notice of Allowance is requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504 (ITL.0627US).

Respectfully submitted,

Date: June 16, 2005

Fred C. Pruner, Jr.
Registration No. 40,779
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Suite 100
Houston, Texas 77024
(713) 468-8880 [Phone]
(713) 468-8883 [Fax]

Attorney for Intel Corporation

8